

PRIOR ART

[illegible]

PRIOR ART

3/6

Fig.4

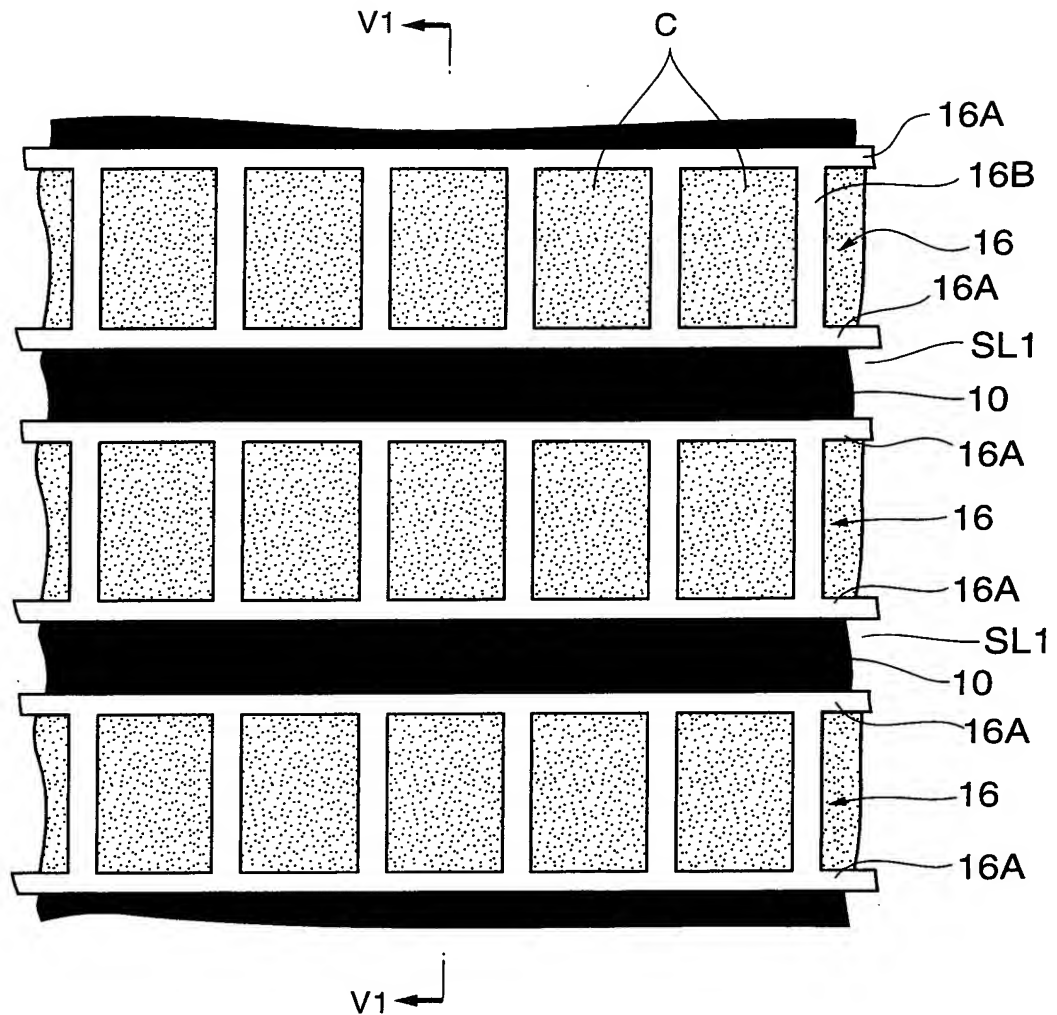


Fig.5

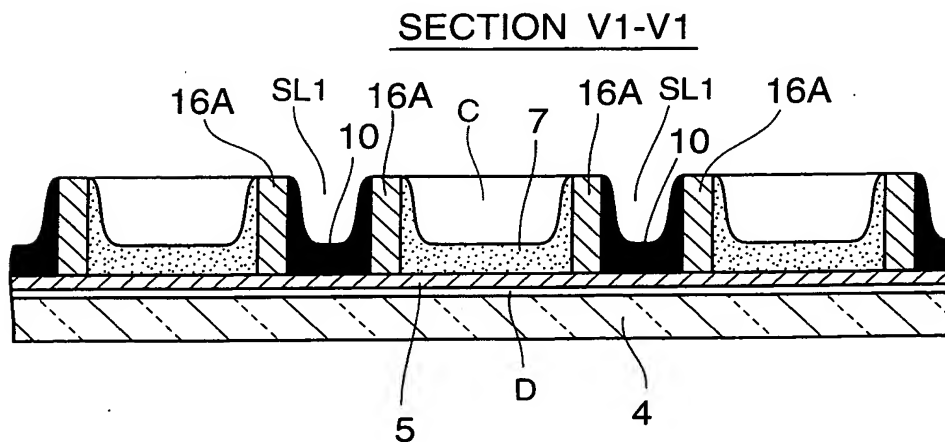
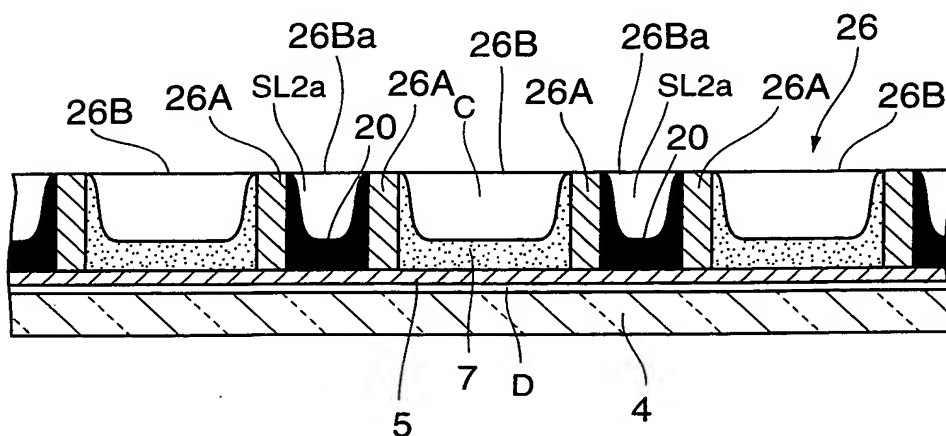


Figure 1 is a plan view of a display panel. It shows a grid of pixels arranged in rows and columns. The grid is defined by horizontal gate lines (SL2) and vertical data lines (C). The pixels are divided into two types: 26A (stippled) and 26B (solid black). The panel is surrounded by a frame (20). A coordinate system (V2) is shown at the top and bottom.

SECTION V2-V2



5/6

Fig. 8

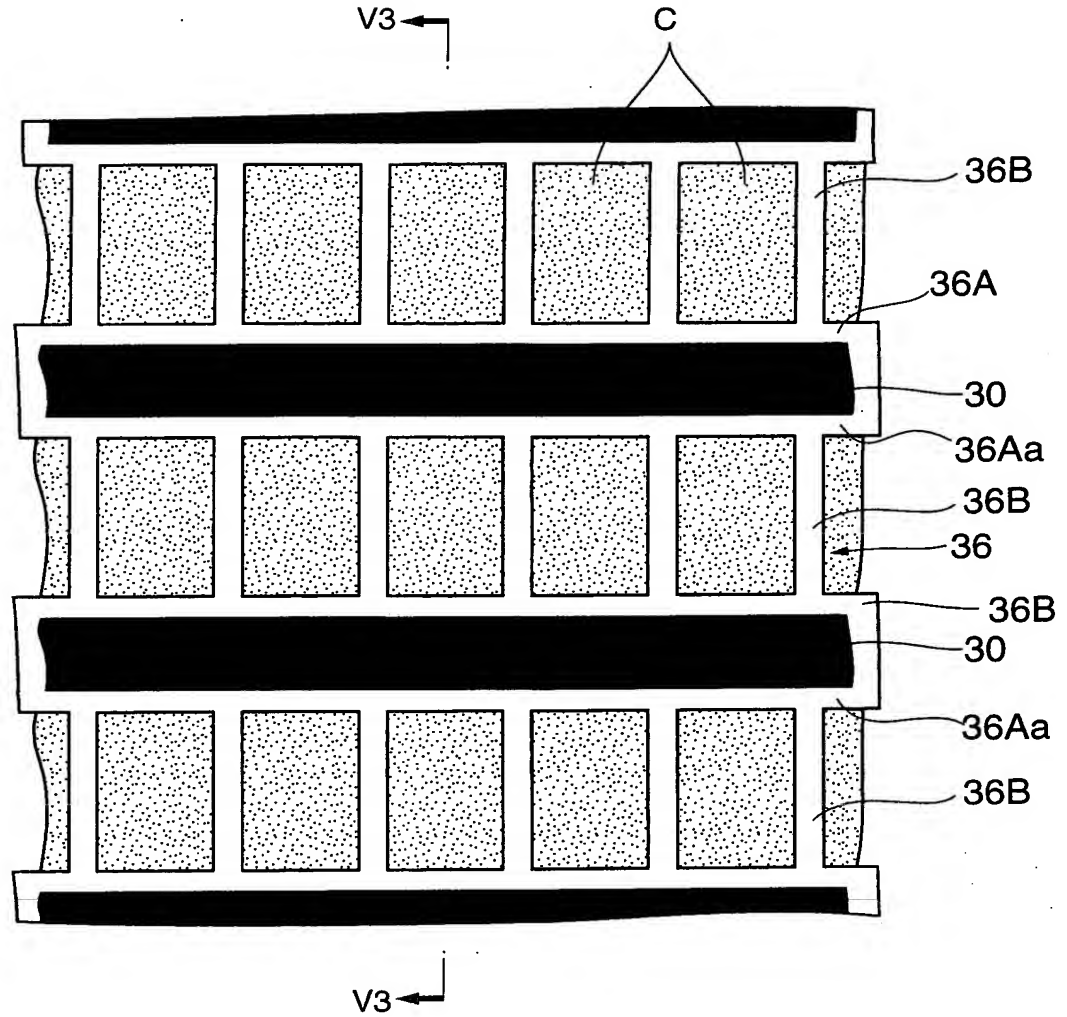


Fig. 9

SECTION V3-V3

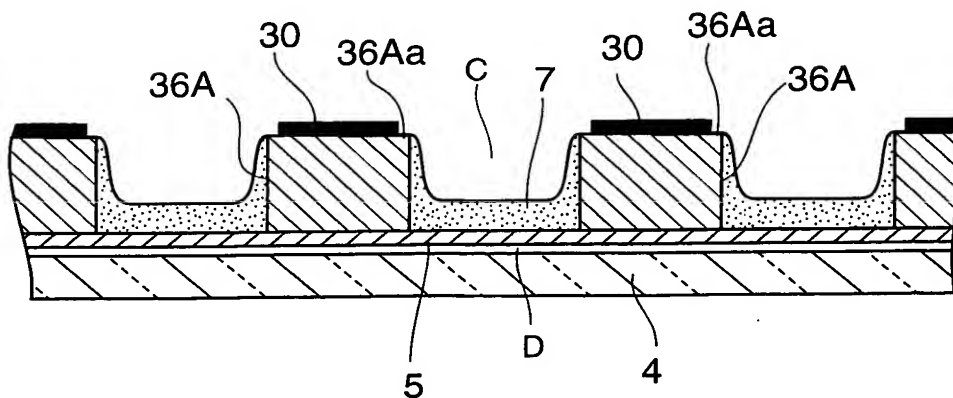


Fig. 10

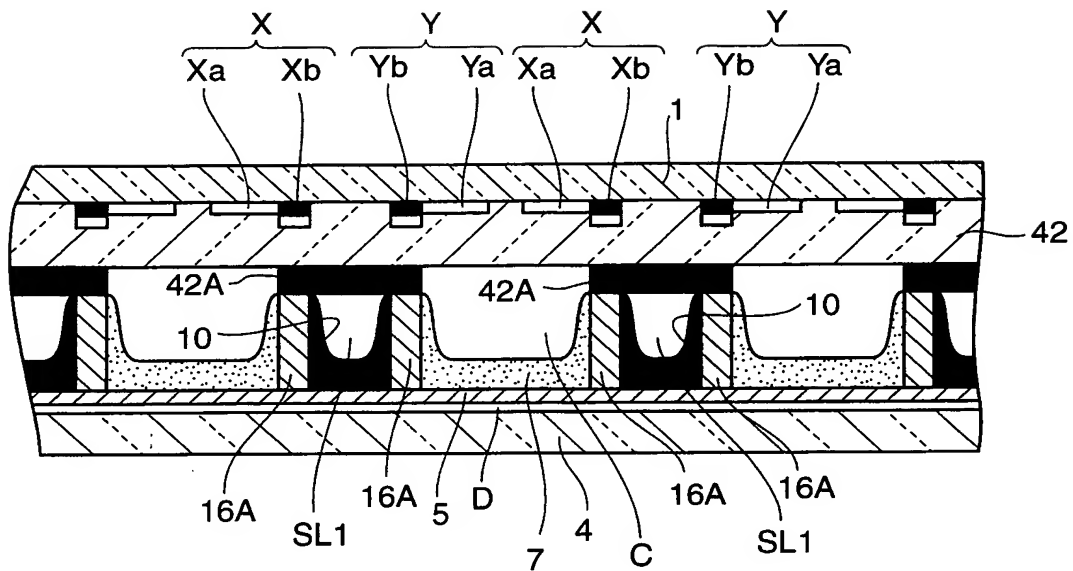


Fig. 11

